

# BALOGH

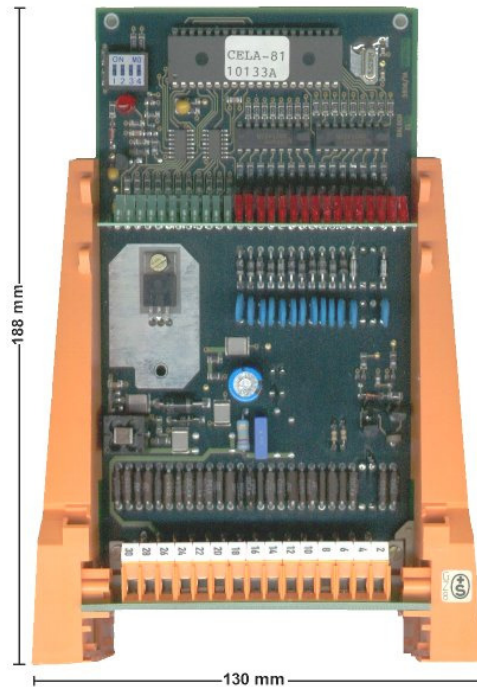


## Control Board CELA-81

### Identification - Coding

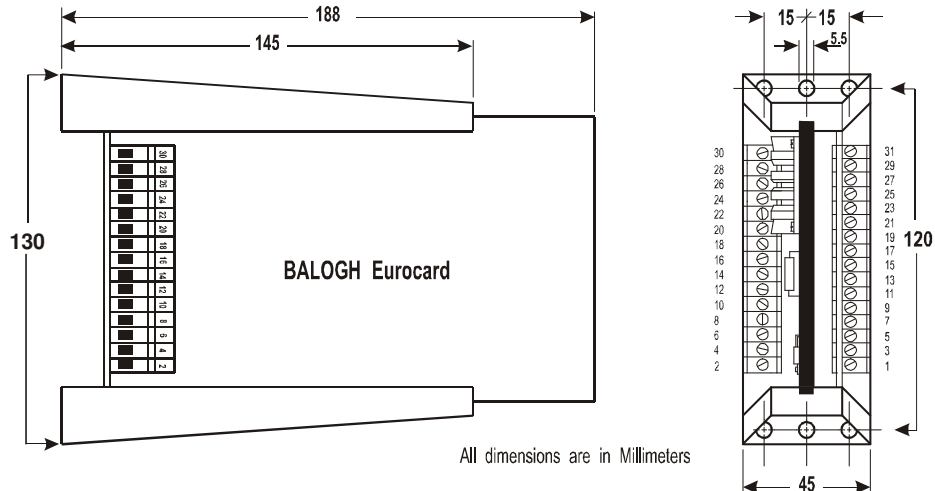
#### Characteristics

- Parallel Control Card, Eurocard Format (100 x 160mm)
- Multiplexable parallel connection
- Allows Reading and Writing of type "OMA" 64 byte TAGS.
- Each Control Board must be connected to a BALOGH Transceiver in order to Read/Write data from the TAGS.
- Requires GC-01 Board Holder



| Characteristics at 25° C                               | Symbol | Unit  | CELA-81      |
|--|--------|-------|--------------|
| V Supply (< 2% Ripple)                                 | Vcc    | V DC  | 24           |
| VoltAGE Tolerance                                      |        |       | -10% to +10% |
| Current Consumption                                    | Im     | MA    | 150          |
| Serial Connection                                      |        |       | No           |
| No. of Parallel Inputs                                 |        |       | 10           |
| Input Impedance  | Ze     | K ohm | 10           |
| Input Logic 0  |        | V     | 0 to 10      |
| Input Logic 1  |        | V     | 15 to Vcc    |
| No. of Parallel Outputs                                |        |       | 13           |
| MAX Continuous Current (per Output)                    | Is     | Ma    | 100          |
| MAX VoltAGE Drop across an Output                      | Vdrop  | V     | 1.5          |
| Output Logic 0   |        | V     | 0            |
| Output Logic 1   |        | V     | Vcc - 1.5    |
| MIN Ambient TEMP                                       | Tmin   | °C    | 0            |
| MAX Ambient TEMP                                       | Tmax   | °C    | +70          |
| Protection Degree                                      | IP     |       | 00           |
| Weight   | M      | g     | 300          |
| MAX Cable Length Between Control Board and Transceiver |        |       | 1000 ft      |
| MAX Cable Length Between Control Board and Buffer      |        |       |              |
| Short Circuit Protected                                |        |       | Yes          |
| Protected against Inverse Polarity                     |        |       | Yes          |

# CELA-81 Control Board



| Terminal | Locations                       | ER*71/85 | ER*80  |
|----------|---------------------------------|----------|--------|
| 1        | Transceiver Output Connected to | E        | Term 3 |
| 2        | Transceiver error Output        |          |        |
| 3        |                                 |          |        |
| 4        |                                 |          |        |
| 5        | Bit 7 Output (MSB)              |          |        |
| 6        | Bit 6 Output                    |          |        |
| 7        | Bit 5 Output                    |          |        |
| 8        | Bit 4 Output                    |          |        |
| 9        | Bit 3 Output                    |          |        |
| 10       | Bit 2 Output                    |          |        |
| 11       | Bit 1 Output                    |          |        |
| 12       | Bit 0 Output (LSB)              |          |        |
| 13       | VAL Output                      |          |        |
| 14       | VAE Output                      |          |        |
| 15       | PRE Output                      |          |        |
| 16       | DEFB Output                     |          |        |
| 17       | Bit 7 Input (MSB)               |          |        |
| 18       | Bit 6 Input                     |          |        |
| 19       | Bit 5 Input                     |          |        |
| 20       | Bit 4 Input                     |          |        |
| 21       | Bit 3 Input                     |          |        |
| 22       | Bit 2 Input                     |          |        |
| 23       | Bit 1 Input                     |          |        |
| 24       | Bit 0 Input (LSB)               |          |        |
| 25       | DCY Input                       |          |        |
| 26       | SBA Input                       |          |        |
| 27       | Transceiver Input Connected to  | S        | Term 2 |
| 28       |                                 |          |        |
| 29       |                                 |          |        |
| 30       | +24 VDC to Board & Transceiver  | V        | Term 1 |
| 31       | Ground                          | O        | Term 4 |